





SURFACE MOUNT SWITCHING DIODE ARRAY

Features

- Fast Switching Speed
- Ultra-Small Surface Mount Package
- For General Purpose Switching Applications
- High Conductance
- Two "BAV99" Circuits In One Package
- Lead Free/RoHS Compliant (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- "Green" Device (Notes 4 and 5)

Mechanical Data

- Case: SOT-363
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- · Polarity: See Diagram
- Marking Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.006 grams (approximate)





Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	
Non-Repetitive Peak Reverse Voltage		V_{RM}	100	V
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V _{RRM} V _{RWM} V _R	75	V
RMS Reverse Voltage		V _{R(RMS)}	53	V
Forward Continuous Current	(Note 1)	I _{FM}	215	mA
Non-Repetitive Peak Forward Surge Current	@ t = 1.0μs @ t = 1.0ms @ t = 1.0s	I _{FSM}	2.0 1.0 0.5	А

SOT-363

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Power Dissipation	(Note 1)	P_{D}	200	mW
Power Dissipation	(Note 2)	P _D	300	mW
Thermal Resistance Junction to Ambient Air	(Note 1)	$R_{ heta JA}$	625	°C/W
Thermal Resistance Junction to Ambient Air	(Note 2)	$R_{ heta JA}$	417	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	$V_{(BR)R}$	75	_	V	$I_R = 2.5 \mu A$
Forward Voltage	V _F		0.715 0.855 1.0 1.25	V	I _F = 1.0mA I _F = 10mA I _F = 50mA I _F = 150mA
Reverse Current (Note 6)	I _R		2.5 50 30 25	μΑ μΑ μΑ nA	$V_R = 75V$ $V_R = 75V$, $T_J = 150$ °C $V_R = 25V$, $T_J = 150$ °C $V_R = 20V$
Total Capacitance	C _T	_	2.0	pF	$V_R = 0$, $f = 1.0MHz$
Reverse Recovery Time	t _{rr}	_	4.0	ns	$I_F = I_R = 10 \text{mA},$ $I_{rr} = 0.1 \times I_R, R_L = 100 \Omega$

Notes:

- Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 2. Device mounted on Alumina PCB, 0.4 inch x 0.3 inch x 0.024 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 3. No purposefully added lead.
- No purposerary added read:
 Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
- 5. Product manufactured with Date Code UO (week 40, 2007) and newer are built with Green Molding Compound. Product manufactured prior to Date Code UO are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.
- 6. Short duration pulse test used to minimize self-heating effect.



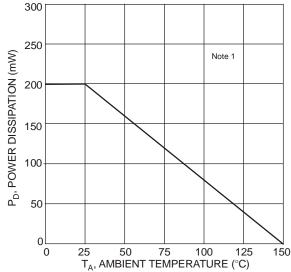
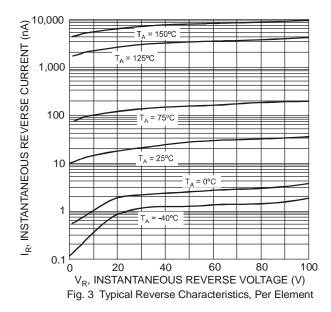


Fig. 1 Power Derating Curve, Total Package



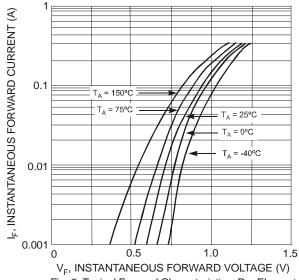


Fig. 2 Typical Forward Characteristics, Per Element

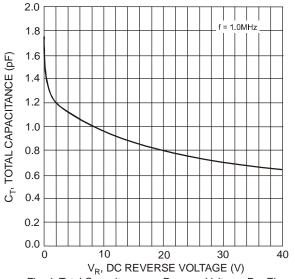


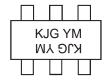
Fig. 4 Total Capacitance vs. Reverse Voltage, Per Element

Ordering Information (Note 7)

Part Number	Case	Packaging
BAV99DW-7-F	SOT-363	3000/Tape & Reel

7. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf. Notes:

Marking Information



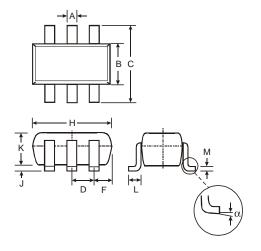
KJG = Product Type Marking Code YM = Date Code Marking Y = Year ex: N = 2002 M = Month ex: 9 = September

Date Code Key

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2111	2012
Code	М	N	Р	R	S	Т	U	V	W	X	Υ	Z
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
				•				_			-	

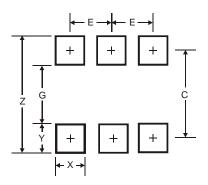


Package Outline Dimensions



SOT-363					
Dim	Min	Max			
Α	0.10	0.30			
В	1.15	1.35			
С	2.00	2.20			
D	0.65 Nominal				
F	0.30	0.40			
Н	1.80	2.20			
J	_	0.10			
K	0.90	1.00			
L	0.25	0.40			
М	0.10	0.25			
α	0°	8°			
All Di	All Dimensions in mm				

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Υ	0.6
С	1.9
Е	0.65

IMPORTANT NOTICE

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to any product herein. Diodes Incorporated does not assume any liability arising out of the application or use of any product described herein; neither does it convey any license under its patent rights, nor the rights of others. The user of products in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on our website, harmless against all damages.

LIFE SUPPORT

Diodes Incorporated products are not authorized for use as critical components in life support devices or systems without the expressed written approval of the President of Diodes Incorporated.